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summary data analysis cycles chip threshold



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[Data analysis techniques for dynamic power simulation of a CPU](#)

MG Blatt, DJ Greenhill, CR Gauthier, K Aingaran - 2003 - freepatentsonline.com

... 0017] The present invention relates to **data analysis** methods for ... In the **summary**, the **Peak** and Low absolute values ... of the MCD, and the power **data** reaches the ...

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Y Yang, ZP Gu, C Zhu, L Shang, RP Dick - Proceedings of the conference on Design, automation and test ..., 2006 - portal.acm.org

... is supported by a hybrid oct-tree **data** structure, which ... Upon initialization, the steady-state ther- mal **analysis** tool generates ... oct-tree based on the **chip** size ...

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T Mudge - Computer, 2001 - ieeexplore.ieee.org

... berkeley.edu/CIC/**summary**/local/**summary**.pdf ... support MPEG4 video transmission and other

data- intensive applications. ... However, the **analysis** shows that if power is ...

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[Introduction to implantable biomedical IC design - all 3 versions »](#)

LJ Stotts, I Inc, TX Austin - Circuits and Devices Magazine, IEEE, 1989 - ieeexplore.ieee.org

... Low-Voltage Circuit Design **Summary** of MOS Device Physics ... simplified but are still very useful for design **analysis**. ... effect can be seen with the **data** plotted in ...

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RJ Baxley - 2005 - etd.gatech.edu

... **SUMMARY** ... this **analysis**, we show that SLM is capable of several Watts of net power ... high-speed equalization was not necessary because **data** was sent in parallel on ...

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[Gate-level power and current simulation of CMOS integrated circuits - all 11 versions »](#)

A Boliolo, L Benini, G de Micheli, B Ricco - Very Large Scale Integration (VLSI) Systems, IEEE ..., 1997 - ieeexplore.ieee.org

... provided by commercial tools for power **analysis** and **simulation** ... **Data** are obtained by electrical-level **simulation** ... In **summary**, the entries of the connection matrix ...

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[HIGH EFFICIENCY LINEAR POWER AMPLIFIERS: ANALYSIS, LINEARIZATION AND IMPLEMENTATION](#)

MF Soltan - 2004 - wireless.stanford.edu

... 7 Table 1-6: **Summary** of a ... duty cycle in PWM.....89 Figure 3 ... to allow better **analysis** and understanding ...

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CMOS low-power analog circuit design

CC Enz, EA Vittoz - Designing **Low Power** Digital Systems, Emerging Technologies (... , 1996 - [ieeexplore.ieee.org](#)

... A first-order **analysis** also shows that the absolute ... Finally, a **summary** and some conclusions are drawn in ... analog filter (continuous or sampled-**data** as switched ...

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Ultra-wideband wireless communications - all 3 versions »

W Zhuang, XS Shen, Q Bi - Wireless Communications and Mobile Computing, 2003 - [doi.wiley.com](#)

... **Summary** ... 1g j—**chip** index of the PN sequence fd n g—information **data** sequence, d ... frames

of bit interval T d . Within each frame, there are N s mono- **cycles**. ...

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Low-Power Multimedia Scheduling Using Output Pre-buffering - all 2 versions »

B Geuntae, K Daewon, P Daeyeon - Modeling, **Analysis**, and **Simulation** of Computer and ... , 2005 - [ieeexplore.ieee.org](#)

... IEEE International Symposium on Modeling, **Analysis**, and **Simulation** ... 4.1 describes how we obtain **cycle** demands of ... tasks, and shows some statistical **data** we have ...

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